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## \*BIBDATASHEET\*

Bib Data Sheet

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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 10/017,414 12/18/2001 PAT 6,605,932 *CL*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 177478/2001 06/12/2001 *CL*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/16/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS 7	INDEPENDENT CLAIMS 1
Verified and Acknowledged Examiner's Signature	<i>CL</i>	Initials			

## ADDRESS

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## TITLE

Voltage booster circuit and semiconductor device for incorporating same

FILING FEE RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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